



memory space located outside processor or m

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[Google Patent Search Help](#)Try uppercase "OR" to search for either of two terms. [\[details\]](#)**Patents** Showing:  View as:  Patents 1 - 10 on **memory space located outside proces**:[Sort by relevance](#) | [Sort by date \(new first\)](#) | [Sort by date \(old first\)](#)**Microprocessor based computer with virtual memory space in overwritable memory**

US Pat. 5511206 - Filed Feb 25, 1994 - Sony Corporation

1 that a data base A and a data base B are stored in the **outside memory** 3, ...  
invention to provide a **microprocessor** controlled **processor** whereby the user ...**Management system for the memory of a processor or microprocessor**US Pat. 4602328 - Filed Dec 17, 1982 - L'Etat Francais represente par le Ministre des P.T.T. (Centre National d'Etudes des  
Telecommunications), Institut National de Recherche en Informatique et en AutomatiqueIn particular, it makes it possible to separate the **located** between the  
**processor** and the physical **memory space** reserved for input - output operations  
and ...**Information exchange processor**

US Pat. 4418382 - Filed May 6, 1980 - Allied Corporation

The lower half as previ- **microprocessor** is to service, and establishes the micro  
- ously explained is called **processor memory space**, and **processor** as either ...**Digital data processor with high reliability**

US Pat. 4654857 - Filed Aug 2, 1985 - Stratus Computer, Inc.

The local address is a selected section of the virtual **memory space** and is ...  
fault signals which originate **outside** the **processor** and are communicated to ...**System for dividing processing tasks into signal processor and decision ...**

US Pat. 5524244 - Filed Feb 15, 1994 - Logic Devices, Inc.

If the user wishes to read or write signal **processor** block diagram parameter ...  
SPROC chip **memory** is 4K of 24-bit words that map to the **microprocessor** as ...**Microprocessor providing an interface between a peripheral subsystem and an ...**

US Pat. 4407016 - Filed Feb 18, 1981 - Intel Corporation

45 Because of its role as an interface between the main **memory space** and the ...  
by the **microprocessor** rather than the peripheral subsystem **processor** for ...**Multi-processor computer system having memory space accessible to multiple ...**

US Pat. 6085273 - Filed Oct 9, 1997 - Thomson Training &amp; Simulation Limited

However, although this enables each bridge 7 to accept and decode **memory space**  
cycles generated by processors other than its local **processor** 5 via the PCI ...**Method for automatically transitioning from V86 mode to protected mode in a ...**

US Pat. 5255379 - Filed Dec 28, 1990 - Sun Microsystems, Inc.

The use of virtual addresses allows addressing of **memory space** in addition to  
... the **processor** operates essentially as a fast 8088 or 8086 **microprocessor**.  
...**High resolution ECG waveform processor**

US Pat. 4630204 - Filed Feb 21, 1984 - Mortara Instrument Inc.

Once the **processor** 304 starts DRAM, PROM, or I/O that the **microprocessor** 304 is  
to ... The acknowl- 35 area in the **memory space** a particular **memory** device ...**Processor core which provides a linear extension of an addressable memory space**

US Pat. 5566308 - Filed May 25, 1994 - National Semiconductor Corporation



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